

T 7

T-OS02

_V2.0

注音事項



		1
1.1		1
1.2		1
		2
2.1		2
2.2		3
2.3		4
		5
3.1		5
3.2		6
3.3		7
3.4		8
		10
4.1		10
4.2		10
4.3		11
4.4		12
4.5		12
		13
5.1		13
5.2		14
		14
6.1		15
6.2		15
6.3		16
6.4		16
6.5 ADD		16
6.6		17
		18
7.1		19
7.2		19

7.3		20
7.4		20
7.5		21
7.6		21

IC

1.1

-
-
-
-
-
-

IC

1.2

DC 24 32V

-25 55

-40 70

≤ 90mA DC 30V

≤ 400mA DC 30V

(H/W/D) 363 × 148.5 × 47mm

CMOS

30W

70 ° × 50 °

0.1 lux

2.5mm

10 °

2.1



2.2



2.3



1 2 TX+ TX- 3 6 RX+ RX-

DC+ GND

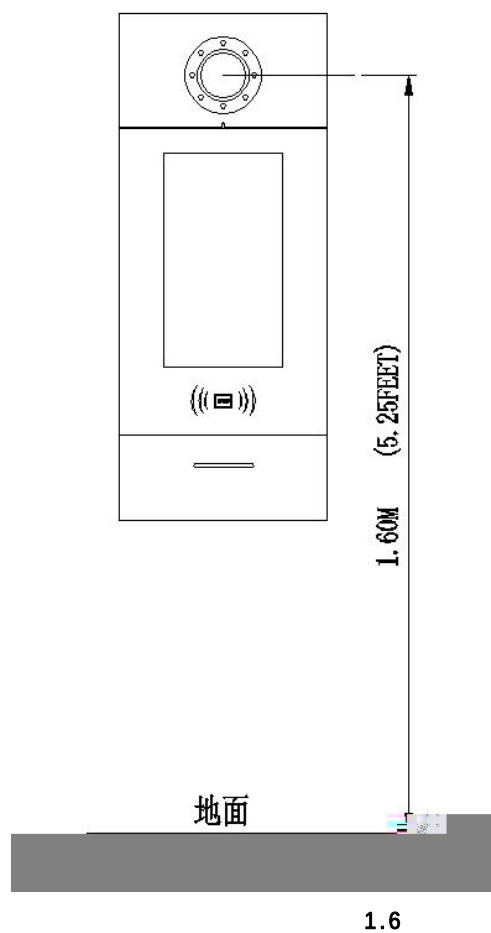
“ 6.4 ”

ADD “ 6.5 ADD ”

BUS-A BUS-B
NO COM NC / /
GS
DOOR SW
GND

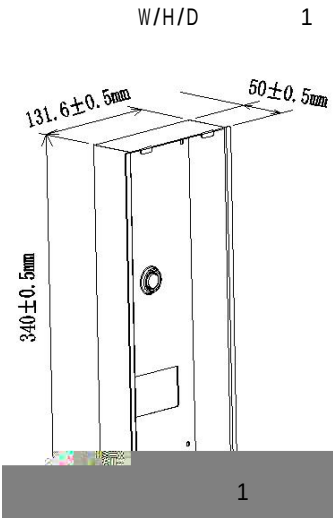
Lock mode

3.1



3.2

2



3

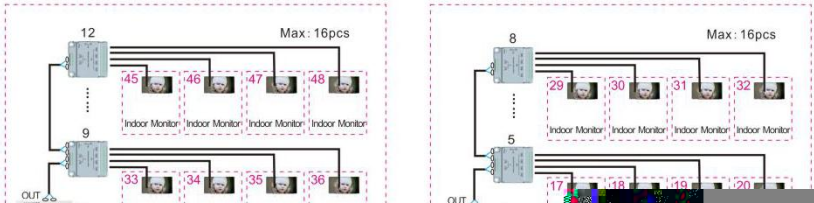


4



6

3.3



①

1 +

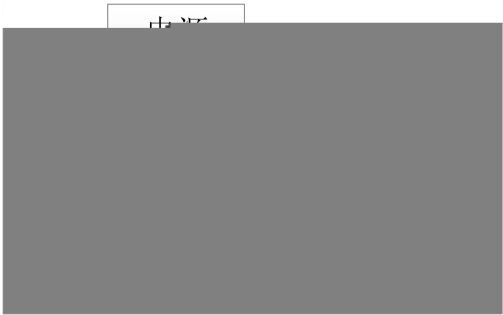
⑥

⑦

1

	+ /
2×0.75^2	160

3.4

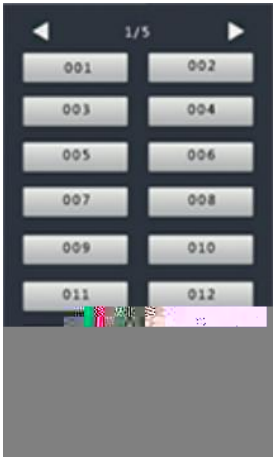




1. GS
2. “ -> -> ”

4.1

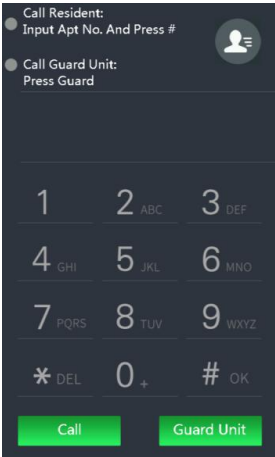
001 199 3
3



4.2



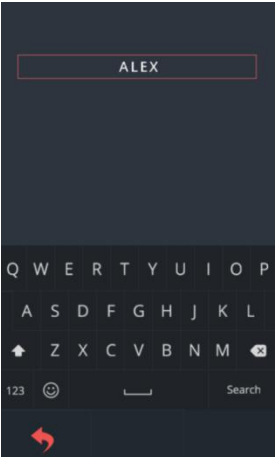
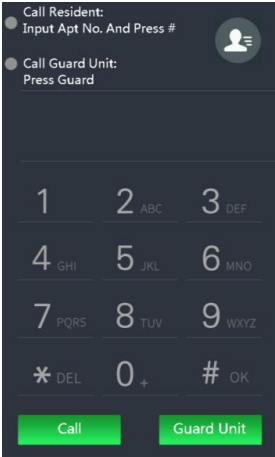
“ ”



0

17

4.3



4.4

Guard Unit

4.5

1)

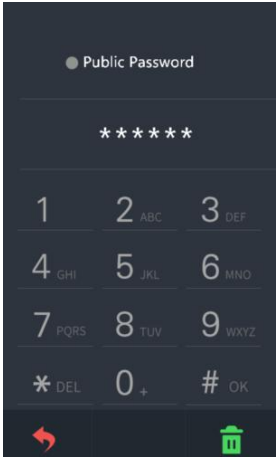
2)

3)

4)

OK

1S





1.

2.

666666

5)



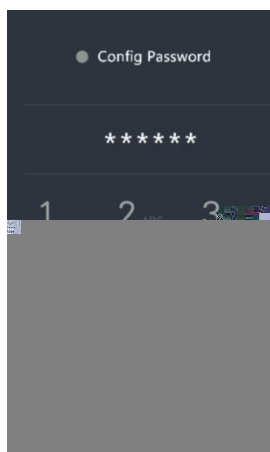
PC

PC

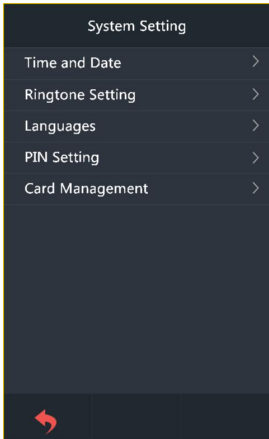
5.1



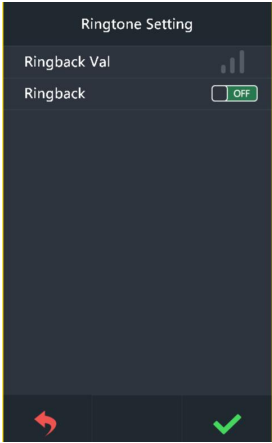
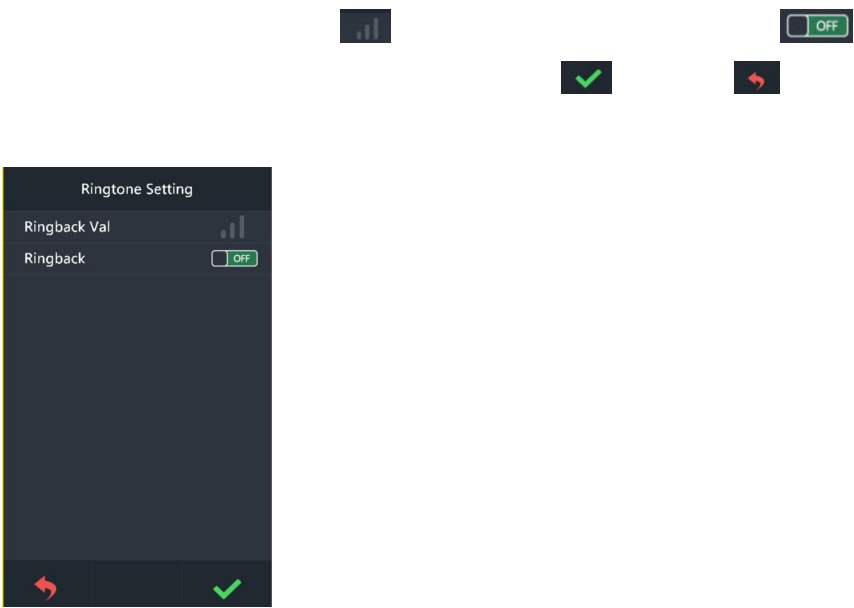
“ 801801 ”



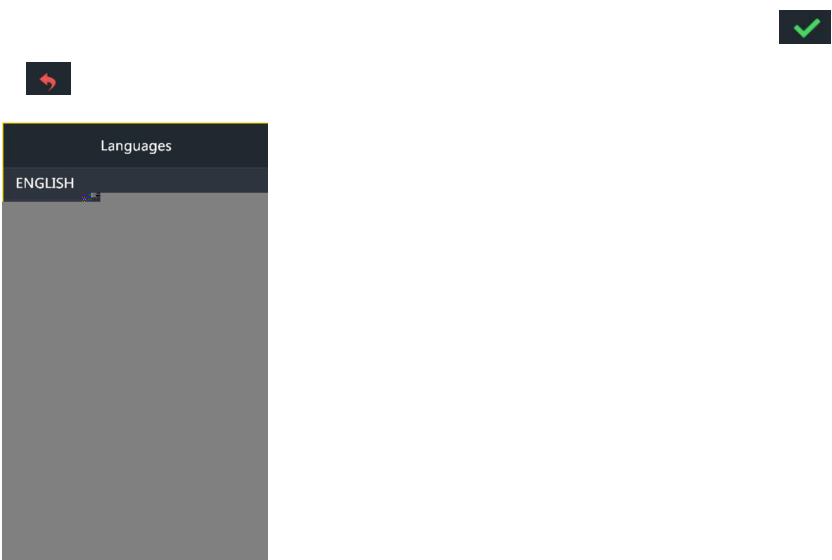
5.2



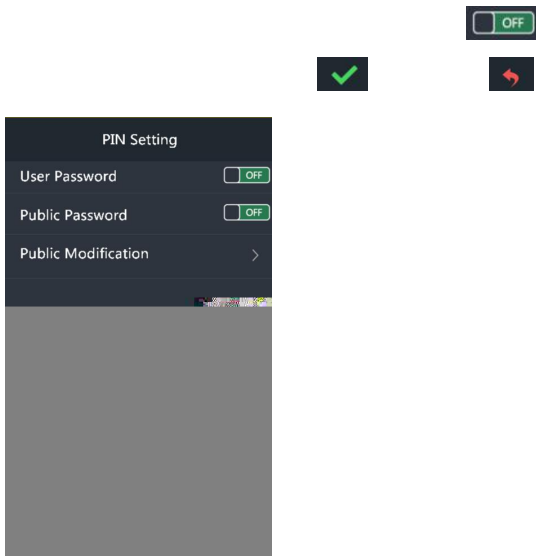
6.1



6.2



6.3



6.4

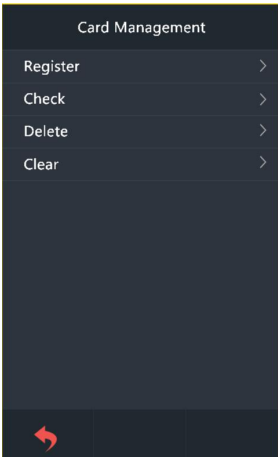
180 °

1S 10S

6.5 ADD

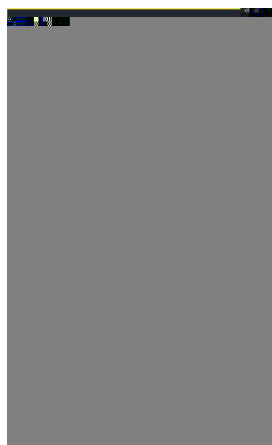
1	2	ADD
OFF	OFF	0
ON	OFF	1
OFF	ON	2

6.6



2000

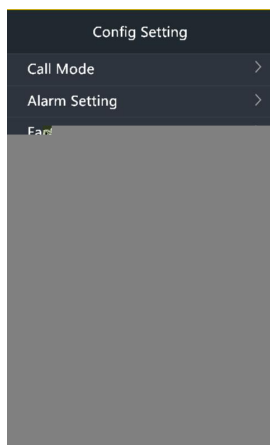




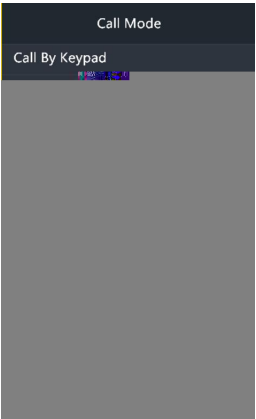
Clear

“

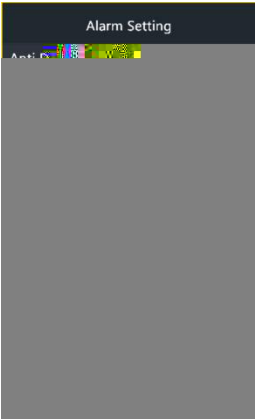
”



7.1



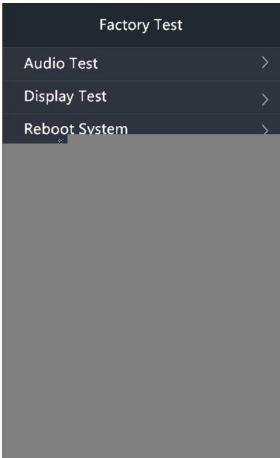
7.2



7.3

120

7.4



1 60S

60S

2

7.5

6



7.6

MCU

